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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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**Complete if Known**

Application Number	10/813239
Filing Date	30-Mar-2004
First Named Inventor	Farkash, Monica
Art Unit	2825
Examiner Name	<b>Bowers</b>
Attorney Docket Number	IL920030060US1

Sheet	1	of	2
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## U. S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

Examiner Signature	/Brandon Bowers/	Date Considered	05/01/2006
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
BB	D	Rudell in "Dynamic Variable Ordering for Ordered Binary Decision Diagrams," Proceedings of the IEEE International Conference on Computer-Aided Design (Santa Clara, California,	
BB	E	Song and Chang "A Variable Reordering Method for Fast Optimization of Binary Decision Diagrams," Proceedings of the Fifth Asian Test Symposium ATS'97 (IEEE, 1997), pp. 228	
BB	F	Bryant and Meinel "Ordered Binary Decision Diagrams in Electronic Design Automation," published in Logic Synthesis and Verification (Kluwer Academic Publishers, 2001), pages	
BB	G	Meinel and Slobodova "Speeding Up Variable Reordering of OBDDs," Proceedings of the International Conference on Computer Design (1997), pages 338-343	
BB	H	Beer et al., in "RuleBase: an Industry-Oriented Formal Verification Tool," Proceedings of the Design Automation Conference DAC'96 (Las Vegas, Nevada, 1996)	

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